Programmable Logic Device With A Memory—Based Finite State Machine Kevin E. Sallese

M-15170 US

REPLACEMENT SHEET 2/3



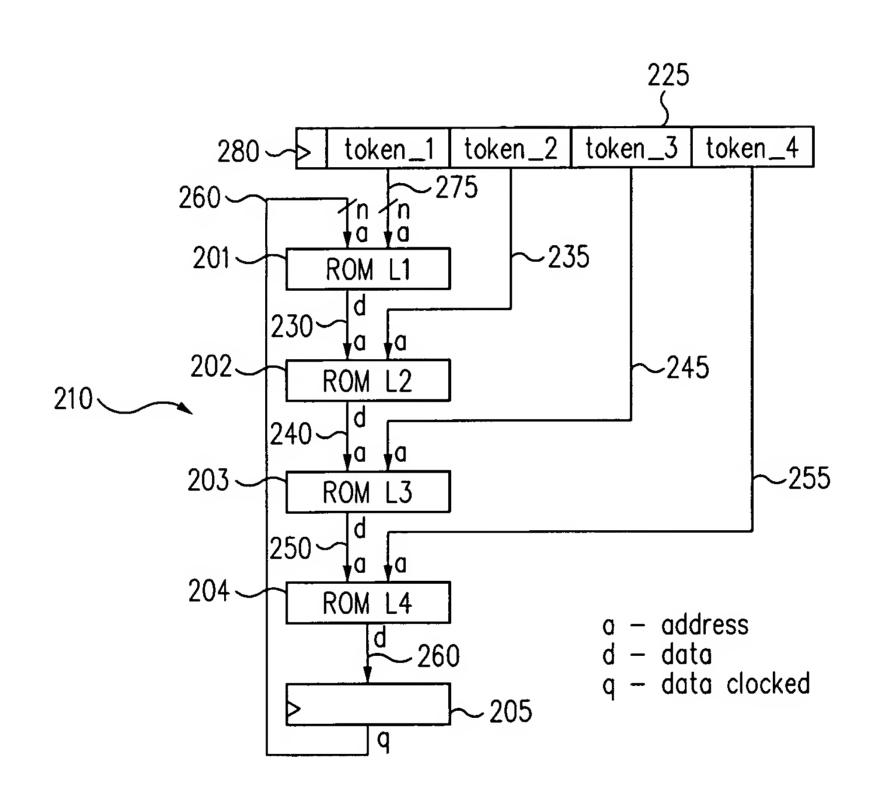
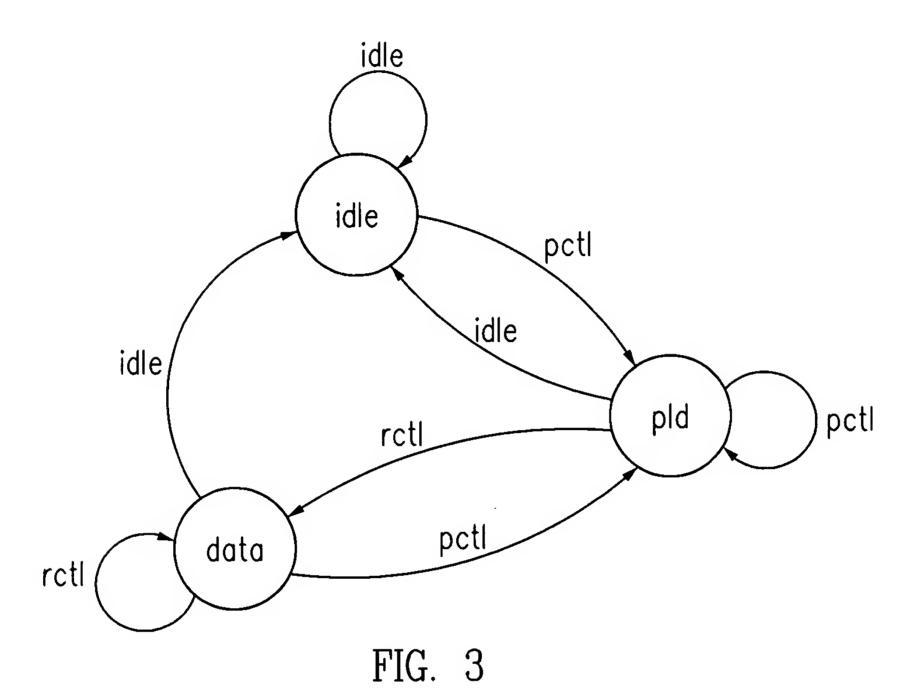


FIG. 2

Programmable Logic Device With A Memory-Based Finite State Machine Kevin E. Sallese
M-15170 US

REPLACEMENT SHEET 3/3





300 305 201 202 203 204

FIG. 4